HE UNITED STATES PATENT AND TRADEMARK OFFICE O

Krishna Rangasayee et al.

08/707,694

PROGRAMMABLE LOGIC DEVICE HAVING AN Application of:

Serial No.:

Title:

INTEGRATED PHASE LOCK LOOP

September 4, 1996 Filed:

Attorney Docket No.: 0325.00063

Butler, D. Examiner:

2787 Art Unit:

Office Action mailed June 27, 2000 In Response To:

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on <u>September 27, 2000</u>.

AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action mailed June 27, 2000, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows: